# TTL OUTPUT HALF SIZE DIP CRYSTAL CLOCK OSCILLATOR

## AHT Series





#### **FEATURES:**

- Hermetically sealed metal package
- TTL output
- Case ground for EMI protection
- Tri-state Enable/Disable options

### **APPLICATIONS:**

- Provide clock signals for microprocessors and digital circuits
- TTL output for low EMI applications

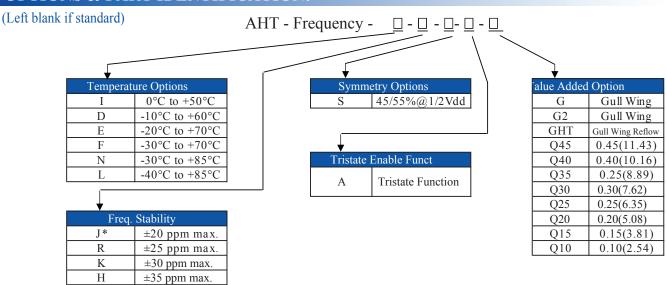
## > STANDARD SPECIFICATIONS:

PARAMETERS		
ABRACON P/N	AHT Series	
Frequency:	400kHz to 160MHz	
Operating temperature:	0°C to +70°C (see options)	
Storage temperature:	- 55°C to + 125°C	
Overall frequency stability*:	± 100ppm max. (see options)	
Supply voltage (Vdd):	$5.0 \text{Vdc} \pm 10\%$	
Input current:	See Table 1	
Symmetry:	40/60% max. (see option)	
Rise and Fall Times (Tr/Tf):	See Table 1	
Output load:	10 TTL max.	
Output Voltage:	VOH = 0.9 * Vdd min. VOL = 0.1* Vdd max.	
Tri State Function:	"1" (VIH >= 2.4 Vdc) or open: Oscillation "0" (VIL < 0.4V): Hi Z	
Output Disable/ Enable time:	100 ns max. (for Option "-A" ONLY)	
Start-up-time:	10 to 35 ms max	

Table 1

Frequency	Input current	Rise/Fall time
MHz	m A	ns
400.000kHz ≤F<24.999MHz	20	10
25.000Hz ≤F<100.000MHz	40	5
100.000kHz ≤F<160.000MHz	80	4

## > OPTIONS & PART IDENTIFICATION:



<sup>\*</sup>Temp option I, D, and E only

±50 ppm max.

C





<sup>\*</sup> Overall frequency stability includes initial tolerance, temperature stability, and aging.

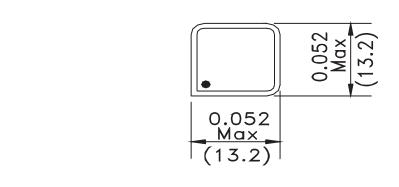
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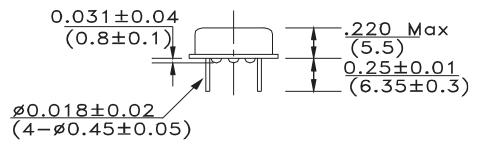


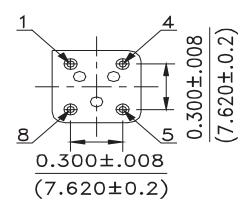




### **OUTLINE DIMENSIONS:**







PIN	FUNCTION
1	NC or Tristate Enable/ Disable
4	GND
5	Output
8	VDD

Note: Recommended using an approximately 0.01uF bypass capacitor between PIN 4 and 8.

Dimensions: inches (mm)

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